

# Ac to Ac Frequency Changer THD Reduction Based on Selective Harmonic Elimination

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#### Abstract

AC to AC frequency converters had a challenge to be proposed as a reduction of THD. This challenge due to eliminate the most effective harmonics in frequency spectrum with low cost and no difficulty. However, the challenge is represented by producing new technique for AC to AC frequency changer with lower total harmonic distortion compared with regular AC to AC frequency changer. This proposed work introduces reduction based on eliminate the 3rd predominant harmonic by imposing a waveform with same frequency, amplitude and poly phase of presented harmful harmonic the elimination for the most effective harmonic will cause reduction in THD. The proposed technique gave a significant reduction in THD (about 52%) with respect to regular AC to AC frequency changer with low cost implementation requirements.

**Keywords:** Power electronics; Cycloconverters; Frequency changers; Harmonic reduction techniques; SPWM

#### Introduction

Variable frequency converters became important now a day with every growing demand of industrial applications [1], in most direct AC to AC frequency changers (step down) high THD has been appeared [2], the increasing in THD occurs due to higher magnitude of predominant harmonics compared with amplitude of fundamental frequency after dividing the frequency of supply source by (M) where M=2, 3, 4,... etc. [3], i.e., for frequency changer with M=2 and main supply frequency of 50 Hz the output frequency of frequency changer will be 25 Hz, the frequency spectrum of the output shown only the odd harmonics with different amplitudes and different phases [4] while the even harmonics had zero magnitude due to even wave symmetry, and harmonic distortion result by the odd harmonics only [5]. One of the important parameters that used to measure the performance in power electronic converters is nth harmonic distortion [6] caused by the each harmonic which can be estimated using  $(\frac{A_n}{A} \times 100)$  where  $A_n$ represent the amplitude of corresponding harmonic while A, represent the magnitude of fundamental, for divide by two frequency changer described above the nth harmonic distortion resulted by apparent harmonics are subjected in Table 1.

According to the Table 1, the 3<sup>rd</sup> harmonic has most effective distortion with respect to other harmonics appeared in frequency spectrum of output waveform for regular frequency changer which is shown in Figure 1a, which frequency spectrum shown in Figure 1b, and the overall distortion result by each apparent harmonics gives THD about 62% [5].

Harmonics generated by AC to AC frequency changers, increase power system heat losses and power bills of end users. These harmonics related losses reduce system efficiency, cause apparatus overheating, and increase power and air conditioning coasts [7], according that harmonic currents can have a significant impact on power electronics

N	F (Hz)	nth Harmonic distortion 100%			
3	75	60.28			
5	125	15.12			
7	175	6.44			
9	225	4.07			

Table 1: nth Harmonic distortion for AC to AC frequency changer with M=2.

devises [8]. However many techniques have been investigated in order to reduce THD of frequency changers such as (Comb Filtering, Multi-Phase Comparative Commutation, Half Cycle Omission, PWM Switching) [9-12], the techniques comb filtering, multi-phase comparative commutation depend on estimating the magnitude of predominant harmonic which caused high THD, and trying to reduce the magnitude of these predominant harmonics in order to reduce THD, however the pervious methods depend on magnitude of harmonics and forgetting the phase shift of predominant harmonic effect, also to implement single phase undistorted output waveform, it is necessary to provide at least two balanced distorted output waveforms, or for the PWM technique the output voltage control can be obtained without any external components and PWM minimizes the lower order harmonics, while the higher order harmonics with high n<sup>th</sup> harmonic distortion has been appeared, which can be eliminated using a filtering techniques, also the value of root mean square voltage of fundamental frequency reduced to significant amount and caused increasing in current that subjected to the load, the previous problem has been appeared in half cycle omission technique, however the problems that described in each technique gives a bad performance for power electronics devices generally and frequency changers specially, according that a new technique to reduce THD of AC to AC frequency changers based on the phase shift of predominant harmonic has been proposed.

# **Proposed Technique**

The suggested method represents the effective predominate harmonic of regular AC to AC frequency changer with M=2 in complex polar form as a magnitude and phase shift the elimination

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process depend on imposing a poly-phase shifted signal with amplitude and frequency equal to magnitude and frequency of harmful third harmonic which can be described in complex form as  $A_3 \angle (\theta_3 + \pi)$ , the complex addition can be determined as shown in phasor diagram of in Figure 2.

Which is shows two complex components one of them represent harmful 3<sup>rd</sup> harmonic and the other represent the imposed poly-phase signal, the resultant of adding these two complex components yields in an elimination of harmful 3<sup>rd</sup> harmonic, which can mathematically represented:

However the proposed system depends on generating the poly phase of 3<sup>rd</sup> harmonic using simple H bridge inverter, and generating distorted waveform using regular AC to AC single phase frequency changer and adding the two singles to-gather as shown in Figure 3.

# Mathematical Model and Calculation

The proposed technique depend on adding two waveforms of different converters which are differs in frequency, phase, and amplitude one of them represents output of regular AC to AC converter operates



with M=2 with frequency can be represented according  $f_c = \frac{f_{supply}}{M}$ which achieve higher THD and the other represents the imposed signal that eliminate the harmful harmonic which is has frequency of  $f_p = \frac{3 \times f_{supply}}{M}$ . The output of the proposed converter can be represented by the following equation:

 $V_{(with low THD)} = V_{(with high THD)+} V_{(3rd harmonic imposed wave)}$ (1)

The Fourier transform for both sides according to linearity theorem can be represented as shown in equation (2):

$$F(jw)_{(with low THD)} = A_1 F(jw)_{(with high THD)} + A_3 F(jw)_{(3rd harmonic imposed wave)}$$
(2)

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Calculation for regular AC to AC converter		Calculation of SPWM inverter			
Order of harmonics for AC to AC converter	Normalized amplitude of harmonic 100%	Phase shift of harmonic	Order of harmonics inverter	Normalized amplitude of harmonic 100%	Phase shift of harmonic
$\substack{ N=1\\ F_N=25 \ Hz }$	100	0.0°	N=1 F <sub>N</sub> = 75 Hz	100	180°
N=3 F <sub>N</sub> = 75 Hz	60	0.0°	N=3 F <sub>N</sub> = 225Hz	30	-90°
N=5 F <sub>N</sub> =125 Hz	15	1800	N=5 F <sub>N</sub> =375 Hz	5	-20°
N=7 F <sub>N</sub> =175 Hz	6	-2.0°	N=7 F <sub>N</sub> =525 Hz	4	-20°
N=9 F <sub>N</sub> =225 Hz	4	1800	N=9 F <sub>N</sub> =675 Hz	2	180°

Figure 4: Amplitude and phase calculations for fundamental and nth harmonic for both converters.

The polar format of Fourier transform estimated, and manipulated at amplitude and phase of each harmonic appears in the frequency spectrum as shown in equation below:

$$F((T_P, T_C)Nw) \angle \theta_{((T_P, T_C)Nw)} = F(T_CNw) \angle \theta_{T_CNw}$$
(3)

Where  $(T_c)$  represent the period of distorted wave result by regular AC to AC frequency step down changer, while  $(T_p)$  represents the period of imposed wave converter, and the frequency spectrum can determined as shown in equation (4):

$$F((T_p, T_c)Nw) = \begin{cases} either F(T_cNw) or F(T_pNw) when (T_pNw) \neq (T_cNw) \\ F(T_cNw) + F(T_pNw) when (T_pNw) = (T_cNw) \end{cases}$$
(4)

However the output of AC to AC frequency with 
$$\frac{f_s}{f_c} = M = 2$$
,

where  $f_c$  represent the output frequency of regular AC to AC frequency changer and  $f_s$  represent main supply frequency, can be represented using equation (5):

$$V_{(\text{with high THD})} = (-1)^{Z} abs(A\sin wt) where \begin{cases} z = 00 \le t \angle (\frac{T_{P}}{2}) \\ z = 1(\frac{T_{P}}{2}) \le t \angle (T_{P}) \end{cases}$$
(5)

The general form for Fourier series expansion to a periodic waveform can be established using equation (6).

$$F(Nw) = a_0 + \sum_{N=1}^{\infty} a_n \cos Nwt + \sum_{N=1}^{\infty} b_n \sin Nwt$$
(6)

Where,

$$a_N = T \int_{-\frac{T_P}{2}}^{\frac{T_P}{2}} V_{(\text{with high THD})} \cos Nt dt$$
(7)

However due to even wave symmetry, the Fourier coefficients  $(a_N)$  and  $(a_0)$  are zeros, so that we need evaluate coefficients  $(b_N)$ , which can be determined according  $(b_N)$  integral, which can be established according to equation (8):

$$b_N = T \int_{-\frac{T_P}{2}}^{\frac{T_P}{2}} V_{\text{(with high THD)}} \sin Nt dt$$
(8)

After estimating the above integrals which is outcome the sine Fourier coefficients of frequency spectrum which represented using equation (9) [13].

$$b_N = \frac{8\sin(\frac{N\pi}{2})}{\pi(4-N^2)}$$

And these coefficients exist for odd integer of N, so that the Fourier series expansion can be established as shown in equation (10):

$$F(Nw) = \sum_{N=1,3,5,\dots}^{\infty} \frac{8\sin(\frac{N\pi}{2})}{\pi(4-N^2)} \sin Nw_C t$$
(10)

As well as the Fourier series expansion of the imposed signal which generated using unipolar SPWM inverter can be represented in equation (11) [14].

$$F(Nw_{p}t) = \sum_{N=1,3,5,\dots}^{\infty} \frac{4}{N\pi} [\cos(N\alpha_{1}) - \cos(N\alpha_{2}) + \cos(N\alpha_{3})\sin Nw_{p}t \quad (11)$$

Where a1, a2, a3 represent the switching angles of unipolar PWM.

The amplitude and phase of fundamental and n<sup>th</sup> harmonics appeared in spectrums according to equations (10 and 11) were denoted in Figure 4, assuming that the imposed waveform with reference signal has poly phase of 3<sup>rd</sup> harmful harmonic of regular AC to AC frequency changer waveform. The THD of each waveform was estimated and gives 62% for waveform that represent regular AC to AC frequency converter, while for imposed waveform gives THD of 48%, finally resultant spectrums according to equations (10) and (11) separately were determined as shown in Figure 5.

$$F((T_{p}, T_{C})Nw) \angle \theta_{((T_{p}, T_{C})Nw)} = \sum_{N=1,3,5,\dots}^{\infty} \frac{8\sin(\frac{2}{2})}{\pi(4-N^{2})} \sin Nw_{C}t \angle \theta_{Nw_{C}}$$
(12)  
+ 
$$\sum_{N=1,3,5,\dots}^{\infty} \frac{-4}{\pi} [\cos(N\alpha_{1}) - \cos(N\alpha_{2}) + \cos(N\alpha_{3})\sin Nw_{p}t \angle \theta_{Nw_{p}}$$

 $n = (N\pi)$ 

And the Fourier series of resultant waveform that established according the proposed technique which is described in equation (1), with low THD can be determined using equation (12):

Finally the output of the proposed AC to AC frequency changer is estimated according to equation (1) and it is denoted in Figure 6.





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The spectrum of output waveform that is denoted in Figure 6, can be estimated using equation (12), the estimated spectrum is shown in Figure 7, which is a combination of the two spectrums described according to equations (10, 11), that is mean the spectrum of regular AC to AC frequency converter and the spectrum of the unipolar SPWM imposed waveform are merged together and the apparent harmonics in the new spectrum have frequencies of (25\*N and 75\*N) Hz respectively where (N=1, 3, 5...). The first harmonic (N=1 fundamental) has a frequency of 25 Hz with suitable normalized amplitude while 3<sup>rd</sup> harmonic which has a frequency of 75 Hz was completely eliminated, finally a bank of high frequencies with low amplitude which have n<sup>th</sup> harmonic distortion does not exceed 12% have been appeared, and the overall THD for proposed converter reduced to 30%.

## Simulation and Result

The proposed system has been implemented using simulink matlab package, the implementation of this system is divided into two

levels: the first one represents a bidirectional bridge converter used to implement regular AC to AC frequency changer which is designed to give 25 Hz high distorted output waveform, while the second level represents rectifier and H bridge circuit used to implement unipolar SPWM inverter which is designed to investigate 75 Hz imposed voltage with phase shift=180° and amplitude equal to amplitude of 3<sup>rd</sup> harmonic related by regular AC to AC frequency changer , the addition process of the two voltages resulted from the two converters has been implemented according to superposition theorem across unity linear transformer. The circuit configuration that represents the proposed technique is described in Figure 8.

The output of each level denoted in the proposed Ac to AC frequency converter circuit is described in Figure 9.

The spectrum of resultant simulated output voltage can be represented using FFT algorithm simulator as shown in Figure 10, which is showing a predominant fundamental frequency of 25 Hz with



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acceptable normalized amplitude, also elimination of harmful third harmonic that appears in frequency spectrum of regular AC to AC frequency changer, the apparent harmonics have frequencies of (25\*N) and (N\*75) respectively and N=1, 3, 5, 7..., with very low normalized voltages. Finally a bank of high order harmonics with low normalized amplitudes has been appeared without any effect on THD value of the proposed converter.

Due to elimination of 3<sup>rd</sup> harmonic, the proposed technique gives output waveform with THD about 30% and this value considered to be significant reduction if it is compared with regular AC to AC frequency changer, the reduction in THD enhanced the performance parameters of converter like (efficiency, system cooling requirement cost, overheating current reduction) according to IEEE standard and recommended values [15,16].

## Conclusion

The proposed system was investigated to give enhanced single phase output waveforms frequency changer if it is compared to the regular AC to AC frequency changer output due to its effective reduction in THD% (about 56% reduction). The proposed system considers being simple and with low cost of implementation, however H bridge inverter used to impose waveform designed to be poly phase and same frequency of 3<sup>rd</sup> harmful and adding the imposed waveform to output of regular AC to AC frequency converter, the addition process will eliminate 3<sup>rd</sup> harmful third harmonic, and result a high frequency harmonic components but with low amplitude and doesn't have significant effect on THD.

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